## ZXMN2A04DN8

## **DUAL 20V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **SUMMARY**

 $\lor$ (BR)DSS=20V; RDS(ON)=0.030 $\Omega$  ; ID=6.8A

#### **DESCRIPTION**

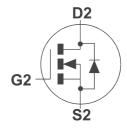
This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



#### **FEATURES**

- Low on-resistance
- · Fast switching speed
- · Low threshold
- · Low gate drive
- Low profile SOIC package

# G1 S1

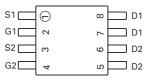


#### **APPLICATIONS**

- DC DC Converters
- Power Management Functions
- Disconnect switches
- Motor control

## ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN2A04DN8TA	7″	12mm	500 units
ZXMN2A04DN8TC	13″	12mm	2500 units



Top View

#### **DEVICE MARKING**

ZXMN 2A04D



## ZXMN2A04DN8

## ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current $(V_{GS}=10V; T_A=25^{\circ}C)(b)(d)$ $(V_{GS}=10V; T_A=70^{\circ}C)(b)(d)$ $(V_{GS}=10V; T_A=25^{\circ}C)(a)(d)$	I <sub>D</sub>	6.8 5.4 5.2	А
Pulsed Drain Current (c)	I <sub>DM</sub>	23	Α
Continuous Source Current (Body Diode) (b)	I <sub>s</sub>	12	Α
Pulsed Source Current (Body Diode)(c)	I <sub>sm</sub>	23	Α
Power Dissipation at $T_a$ =25°C (a)(d) Linear Derating Factor	P <sub>D</sub>	1.25 10	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (a)(e) Linear Derating Factor	P <sub>D</sub>	1.8 14	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (b)(d) Linear Derating Factor	P <sub>D</sub>	2.1 17	W mW/°C
Operating and Storage Temperature Range	$T_j:T_{stg}$	-55 to +150	°C

## THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	R <sub>eJA</sub>	100	°C/W
Junction to Ambient (a)(e)	R <sub>eJA</sub>	70	°C/W
Junction to Ambient (b)(d)	R <sub>eJA</sub>	60	°C/W

### **NOTES**

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t≤10 secs.
- (c) Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10 $\mu$ s pulse width limited by maximum junction temperature. Refer to Transcient Thermal Inpedance graph.
- (d) For device with one active die
- (e) For device with two active die running at equal power.



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# ELECTRICAL CHARACTERISTICS (at T<sub>A</sub> = 25°C unless otherwise stated).

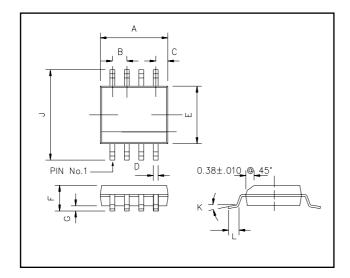
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
STATIC	•	•	•	•	•		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	20			V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			0.5	μА	$V_{DS}=20V, V_{GS}=0V$	
Gate-Body Leakage	I <sub>GSS</sub>			100	nA	$V_{gs}=\pm 12V, V_{ds}=0V$	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	0.7			V	$I_{D} = 250 \mu A, V_{DS} = V_{GS}$	
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.030 0.045	ΩΩ	$V_{GS}$ =4.5V, $I_{D}$ =11A $V_{GS}$ =2.5V, $I_{D}$ =5A	
Forward Transconductance (3)	$g_{fs}$		40		S	$V_{DS}=10V,I_{D}=6A$	
DYNAMIC (3)		•	•	•	•		
Input Capacitance	C <sub>iss</sub>		2300		pF	V 15V V 0V	
Output Capacitance	C <sub>oss</sub>		450		pF	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		260		pF		
SWITCHING(2) (3)	•	•	'	•	•		
Turn-On Delay Time	t <sub>d(on)</sub>		6.3		ns	$V_{DD} = 10V, I_{D} = 6A$ $R_{G} = 6.0\Omega, V_{GS} = 5V$	
Rise Time	t,		8.5		ns		
Turn-Off Delay Time	t <sub>d(off)</sub>		25		ns	$R_{\rm G}=6.0\Omega$ , $V_{\rm GS}=5V$	
Fall Time	t <sub>r</sub>		5		ns		
Gate Charge	$Q_g$		19.4		nC	$V_{DS} = 15V, V_{GS} = 5V, I_{D} = 3.5A$	
Total Gate Charge	Q <sub>g</sub>		24		nC	$V_{DS} = 10V, V_{GS} = 4.5V,$ $I_{D} = 6A$	
Gate-Source Charge	Q <sub>gs</sub>		5		nC		
Gate-Drain Charge	Q <sub>gd</sub>		4		nC		
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	V <sub>SD</sub>		TBA?	0.95	V	$T_J = 25^{\circ}C, I_S = 5.1A, V_{GS} = 0V$	
Reverse Recovery Time (3)	t <sub>rr</sub>		15		ns	T <sub>J</sub> =25°C, I <sub>F</sub> =6A, di/dt= 100A/μs	
Reverse Recovery Charge (3)	Q <sub>rr</sub>		5		nC		

#### **NOTES**

- (1) Measured under pulsed conditions. Width=300 $\mu s.$  Duty cycle  $\leq~2\%$  .
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



## ZXMN2A04DN8 **PACKAGE DIMENSIONS**



DIM	Millimetres		Inches		
	Min	Max	Min	Max	
Α	4.80	4.98	0.189	0.196	
В	1.27 BSC		0.05 BSC		
С	0.53 REF		0.02 REF		
D	0.36	0.46	0.014	0.018	
E	3.81	3.99	0.15	0.157	
F	1.35	1.75	0.05	0.07	
G	0.10	0.25	0.004	0.010	
J	5.80	6.20	0.23	0.24	
K	0°	8°	0°	8°	
L	0.41	1.27	0.016	0.050	



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